Robust modular control system design using an inner-loop reference model and $\mu$ synthesis techniques

Chris Vermillion$^{1,*}$, Jing Sun$^3$ and Ken Butts$^2$

$^1$Altaeros Energies, Boston, MA 02210, USA
$^2$Toyota Technical Center, Ann Arbor, MI 48105, USA
$^3$Department of Naval Architecture and Marine Engineering, University of Michigan, Ann Arbor, MI 48109, USA

SUMMARY

For complex dynamic systems, a modular control design process is often employed, wherein the overall design is partitioned into smaller modules. This paper considers a particular inner-loop/outer-loop modular control strategy in which the designer of the outer-loop module does not know the specifics of the inner loop but instead possesses a reference model that captures the ideal inner-loop input–output behavior. In the first part of this paper, we establish analytical properties of the modular reference-model-based design. In the second part, we introduce a novel mechanism, referred to as the modular control error compensation, which mitigates the performance loss that arises when the inner-loop reference model is not matched. We propose an iterative algorithm, using $\mu$ synthesis, to design this compensator to reduce performance loss on the basis of two concrete worst-case performance metrics. The effectiveness of the modular control strategy with the modular control error compensation is demonstrated through experimental results on an automotive system.

1. INTRODUCTION

To address the complexities of many modern industrial systems, modular control designs are commonly used, where the overall control system is partitioned into multiple modules, each of which addresses a subset of the overall system dynamics. A very common modular strategy is an inner-loop/outer-loop strategy, where an outer-loop controller is designed to achieve overall system performance objectives whereas an inner-loop controller is used to allocate control inputs among individual actuators. This strategy has seen widespread application in the automotive [1, 2], aerospace [3–7], and marine [8–12] industries. Additionally, theoretical work on inner-loop/outer-loop cascade control designs also exists in [13] and [14].

The particular inner-loop/outer-loop strategy and corresponding nomenclature for this paper are shown in Figure 1, where the ultimate objective is to drive the performance variable, $y \in \mathbb{R}$, to a given setpoint, $r \in \mathbb{R}$. The outer-loop controller computes a desired overall effect, $v_{\text{des}} \in \mathbb{R}$, whereas the inner-loop control allocation determines control inputs, $u \in \mathbb{R}^p$, for the actuators, to closely track $v_{\text{des}}$. $v$ is commonly referred to as the virtual control input (and will be in this work), which represents an overall force, moment, or other generalized effect that drives the dynamics of the system. The control strategy in Figure 1 is widely employed in the industry because of its practicality in addressing the plant dynamics and actuator dynamics through separate controllers, particularly when different vendors are responsible for different components. The modular control
strategy splits the design work across different areas of expertise, allows plug-and-play application of different inner-loop modules, and reduces the complexity of the control allocation task.

When the actuator dynamics are very fast relative to the plant dynamics and overall closed-loop system requirements, the modular approach can easily be carried out by two designers in parallel. However, parallel design and seamless integration of the inner-loop and outer-loop controllers represent challenges when the inner-loop dynamics are significant relative to the outer-loop dynamics and exact tracking of $v_{des}$ is impossible. Different integration mechanisms have been proposed in the literature, each with its own benefits and pitfalls. For example, Luo et al. [7] and Tjonnas and Johansen [12] proposed algorithms that achieve asymptotic tracking of $v_{des}$ by performing a target state calculation at every instant and then designing the inner-loop controller to asymptotically track that target state trajectory. The ability to track $v_{des}$ asymptotically, however, requires advanced information about $v_{des}$ to be passed to the inner loop. These strategies, however, do not assess the implications of the delay, which can be significant in the presence of significant inner-loop/outer-loop timescale separation, which must be introduced at the outer-loop level to provide sufficient advanced information to the inner loop. The strategies in [13] and [14] provide systematic design approaches for parallel outer-loop and inner-loop designs but also restrict the form that these controllers may take.

In this paper, we propose an alternative system integration approach to those proposed in earlier literature, which allows outer-loop and inner-loop designers to work in parallel. The proposed strategy does not place restrictions on the design methodology for either controller and does not require significant timescale separation between the inner and outer loops. In the proposed strategy, we introduce an inner loop reference model to capture the target inner-loop behavior from $v_{des}$ to $v$. This reference model can be tailored to reflect dynamics that are both desirable and realistic for the inner loop and can, under appropriate conditions, be matched exactly. In our analysis, we will derive, through a constructive proof, necessary and sufficient conditions on the reference model and actuator dynamics under which it is possible to match the reference model behavior. We also show that if the same conditions hold for the actuator dynamics, then the modular control strategy is capable of achieving equivalent performance to its centralized counterpart, shown in Figure 2. The ultimate result of this two-part analysis will be a set of ideal modular control conditions, which provide us with guidelines on how to choose the reference model and insight with regard to the features of the actuator dynamics that will cause difficulty in modular control design.

Because exact inner-loop model matching is not possible in many circumstances that involve uncertainties or nonideal properties of the actuator dynamics, the second part of this work proposes robust control techniques to manage this mismatch between the ideal and actual performance. Here,
we capture the performance mismatch between the desired and actual inner closed loop through an uncertainty model and propose a novel control strategy, modular control error compensation (MCEC), to reduce the performance mismatch through use of an add-on compensator. This mechanism is inspired by the original work in [15], which considers a similar compensation mechanism but is restricted to parametric uncertainty. The MCEC design proposed in this work, which builds upon ideas from [15] and our earlier work in [16], uses $\mu$ synthesis for the compensator design. This results in a design framework that is applicable to a broader class of uncertainties and allows us to optimize performance on the basis of two concrete objectives. Furthermore, unlike other applications of $\mu$ synthesis such as in [17, 21], which impose an inherent trade-off between nominal performance and performance in the presence of uncertainty, our approach does not sacrifice nominal performance in achieving performance recovery in the presence of uncertainty. This is made possible by the fact that an add-on compensator, rather than the main outer-loop controller, is used to achieve performance recovery. We demonstrate the effectiveness of this strategy through experimental results on an automotive application example. Through this example, we also show how the usage and proper choice of an inner-loop reference model represents a key step in containing and ultimately reducing the mismatch between the ideal and actual system behavior.

The paper is organized as follows. In Section 2, we conduct an in-depth analysis of the reference-model-based modular control design, deriving necessary and sufficient conditions for reference model matching and then demonstrating the close relationship between these conditions and a set of sufficient conditions for modular versus centralized equivalence. In Section 3, we treat the circumstances where the inner closed loop does not match the ideal behavior specified by the reference model. Here, we formally introduce MCEC and the uncertainty model that is used to describe the inner-loop performance mismatch. In Section 4, we use this uncertainty model to formulate a $\mu$ synthesis problem to optimally design MCEC from two concrete worst-case performance metrics. Finally, in Section 5, we present design, analysis, and experimental results for modular control with MCEC on an automotive thermal management system.

2. REFERENCE-MODEL-BASED MODULAR CONTROL DESIGN

This section details the reference-model-based modular design framework, which is depicted in Figure 3. Mathematically speaking, the system in Figure 3 is identical to that in Figure 1; however, the block diagram has been manipulated in Figure 3 to illustrate the reference-model-based design process. Here, the inner-loop reference model, $F(s)$, represents the ideal performance from the desired virtual control input, $v_{\text{des}}$, to the virtual control input, $v$. $x_1 \in \mathbb{R}^{n_1}$ represents the plant states, and $x_2 \in \mathbb{R}^{n_2}$ represents the actuator states, whereas $x_1^m$ and $x_2^m$ represent those plant and actuator states, respectively, that are measured. $\tilde{v}$ represents the difference between the virtual control input, $v$, and the output of the reference model, $v_{\text{des}}^f$; It is therefore the objective of the inner-loop controller to drive $\tilde{v}$ to 0.

To characterize the performance achieved by the reference-model-based modular control design and quantify its design limitations, we will consider linear time-invariant systems whose dynamics are represented as follows:

$$X_1(s) = P^{X_1}(s)V(s),$$  \hspace{1cm} (1)

$$Y(s) = P^Y(s)V(s),$$  \hspace{1cm} (2)

$$X_2(s) = A_{u}^{X_2}(s)U(s) + A_{x_1}^{X_2}(s)X_1(s),$$  \hspace{1cm} (3)

$$V(s) = A_{u}^{V}(s)U(s) + A_{x_1}^{V}(s)X_1(s),$$  \hspace{1cm} (4)

where $P^Y(s)$ is a scalar transfer function; $P^{X_1}(s)$, $A_{u}^{X_2}(s)$, and $A_{x_1}^{X_2}(s)$ are $n_1 \times 1$, $1 \times p$, and $1 \times n_1$ transfer function vectors, respectively; and $A_{u}^{X_2}(s)$ and $A_{x_1}^{X_2}(s)$ are $n_2 \times p$ and $n_2 \times n_1$ transfer function matrices, respectively. The outer-loop and inner-loop control laws are given by

$$V_{\text{des}}(s) = C_{r}^{\alpha}(s)R(s) + C_{x_1}^{\alpha}(s)X_1(s)$$ \hspace{1cm} (5)
and

\[ U(s) = C^i_{v_{des}}(s)V_{des}(s) + C^i_{x_2}(s)X_2(s), \]  

respectively, where \( C^o(s) \) is a scalar transfer function, \( C^o_{x_1}(s) \) and \( C^i_{v_{des}}(s) \) are \( 1 \times n_1 \) and \( p \times 1 \) transfer function vectors, respectively, and \( C^i_{x_2}(s) \) is a \( p \times n_2 \) transfer function matrix.

**Remark 1**
In the absence of full state measurements, the entries in \( C^o_{x_1}(s) \) and \( C^i_{x_2}(s) \) corresponding to unmeasured states will be equal to 0. Thus, we will typically omit the \( x^m_1 \) and \( x^m_2 \) notations throughout the paper, without loss of generality, to simplify the presentation.

Given an inner-loop controller \((C^i_{v_{des}}(s) \text{ and } C^i_{x_2}(s))\), the closed-inner-loop input–output dynamics can be described by

\[ V(s) = T_1(s)V_{des}(s) + T_2(s)X_1(s), \]

where, suppressing the \( s \) notation for simplicity, we have

\[ T_1 = A^v_u(I - C^i_{x_2}A^x_{x_2})^{-1}C^i_{v_{des}}, \]

\[ T_2 = A^v_u(I - C^i_{x_2}A^x_{x_2})^{-1}(C^i_{x_2}A^x_{x_1}X_1 + A^v_{x_1}). \]

For the modular reference-model-based control strategy, when the inner-loop reference model is matched, we have

\[ T_1(s) = F(s), \]

\[ T_2(s) = 0, \]

which we will refer to as the *nominal* closed inner loop. The notion of relative degree will be important to our subsequent analysis, and we will use the following scalar definition of relative degree.

**Definition 2**
A \( 1 \times q \) transfer function vector, \( H(s) \), has relative degree \( \rho(H(s)) = \min_i \rho(H_i(s)) \), where, taking \( H_i(s) = N(s)/D(s) \), \( \rho(H_i(s)) = \deg(D(s)) - \deg(N(s)) \).

Thus, we take the relative degree of transfer function vectors as the *lowest* among the input–output pairs.
2.1. Overall system performance metric

We will use the following metric for evaluating the overall system performance.

**Definition 3 (overall performance index)**

Let $G_y(s)$ and $G_{des}(s)$ represent the actual and desired transfer functions, respectively, from $r$ to $y$, and let $G^v(s)$ represent the actual transfer function from $r$ to $v$, and assume that these transfer functions are stable. The performance level, $\gamma^p$, of the closed-loop system is defined as

$$\gamma^p \triangleq \left\| W_y(s)(G_y(s) - G_{des}(s)) W^v(s)G^v(s) \right\|_{\infty},$$

where $W_y(s)$ and $W^v(s)$ are stable weighting transfer functions that emphasize or penalize performance at different frequencies.

This performance metric places importance on both setpoint tracking and the size of the virtual control input that is to be generated by the inner loop. In this section, we consider this performance on the nominal system, in the absence of uncertainty; however, subsequent sections treat the problem of recovering this nominal performance, to the greatest extent possible, in the presence of nonparametric uncertainty in the inner-loop dynamics.

2.2. Inner-loop performance metric

Because the objective of the inner-loop controller is to achieve $T_1(s) = F(s)$ and $T_2(s) = 0$, the performance of the closed inner loop can be evaluated by

$$C^i = \arg \min_{C^i_{des}, x^2} \gamma^i,$$

$$\gamma^i = \left\| W^1(s)(F(s) - T_1(s)) W^2(s)T_2(s) \right\|_{\infty}.$$

Although we do not place any restrictions on the inner-loop control structure, we do derive a model-matching controller for cases when $\gamma^i = 0$ is achievable (through a constructive model-matching proof). We also derive both necessary and sufficient conditions for model matching to be achievable.

2.3. Conditions for reference model matching

As a first step of our analysis, we determine the conditions under which reference model matching, that is, $\gamma^i = 0$, can be achieved.

**Proposition 4 (model-matching conditions)**

Suppose that the states $x^m_2$ are available for measurement, that is, $x^m_2 = x_2$. Then, given an arbitrary, stable $F(s)$, there exists a causal and stabilizing inner-loop controller (i.e., proper and stabilizing transfer function matrices $C^i_{des}$ and $C^i_{x^2}$) that results in $V(s) = F(s)V_{des}(s)$ if and only if the following conditions exist:

1. $\rho(A^u_0(s)) \leq \rho(F(s))$.
2. $\rho(A^u_{x^1}(s)) < \rho(A^u_{x^2}(s))$.
3. There are no unstable zeros common to all $p$ transfer functions in $A^u(s)$.

The detailed proof is provided in the Appendix. The proof of sufficiency involves construction of a controller that achieves model matching and stability. In particular, although a single control input is sufficient to achieve model matching, the proof shows that additional control inputs (if $p \geq 2$) can be used to stabilize the inner loop in the presence of nonminimum phase zeros. The necessity proof is quite straightforward and makes use of the notions of relative degree and stable pole-zero cancelations.

Because the reference model, $F(s)$, is a design choice, condition (1) in Proposition 4 can always be achieved and therefore does not restrict the set of systems for which model matching can be
achieved. Condition (2) essentially requires the disturbance from $x_1$ to affect $v$ more slowly than $u$, such that state feedback can be used to reject this disturbance. In practice, there exist plants where this scenario is violated, and it is important, therefore, to address scenarios where system features or uncertainties lead to a mismatch between the reference model and actual inner-loop behavior (i.e., where $F(s) \neq T_1(s)$ or $T_2(s) \neq 0$). For this reason, Sections 3 and 4 of this paper are dedicated to performance recovery in these instances. Finally, condition (3) ensures that reference model matching can be achieved without introducing internal instability into the inner loop. The assumption degenerates into a very standard nonminimum phase model reference control assumption for a single-input, single-output inner loop, but multiple actuators provide greater flexibility in relaxing this condition.

2.4. Modular versus centralized performance

In this section, we compare the performance level of the modular control strategy with that of its centralized counterpart. For our performance comparison, we introduce the following notation.

Definition 5

Let $\gamma^p_o$, $\gamma^c_o$, and $\gamma^p_m$ represent the values of $\gamma^p$, as defined in (12), achieved with a given outer-loop controller when $T_1 = F$ and $T_2 = 0$ (i.e., ideal modular control), with centralized control and with modular control, respectively.

Our comparison of these three levels of performance is given in the following proposition.

Proposition 6 (modular versus centralized performance inequality)

Assume that the optimal levels of performance achievable (i.e., the lowest values of $\gamma^p$, as defined in Definition 3) by the ideal modular design, centralized control system, and modular control system, under causal and stabilizing controllers, are given by $\gamma^*_o$, $\gamma^*_c$, and $\gamma^*_m$, respectively, and suppose that $F(s)$ is stable, is a minimum phase, and satisfies $\rho(F(s)) \leq \rho(A_u^o(s))$ and $\rho(F(s)) \leq \rho(A_{x1}^o(s))$. Then, $\gamma^*_o \leq \gamma^*_c \leq \gamma^*_m$.

Proof

$\gamma^*_o \leq \gamma^*_m$: Because the modular control system is a special case of the centralized controller, $\gamma^*_c \leq \gamma^*_m$ follows immediately.

$\gamma^*_o \leq \gamma^*_c$: Consider an optimal centralized control law given by

$$ U^*(s) = C_f^c(s)R(s) + C_{x1}^c(s)X_1(s) + C_{x2}^c(s)X_2(s), $$

(15)

which yields $\gamma^o = \gamma^c$. The corresponding closed-loop system transfer functions, $G^o(s)$ and $G^c(s)$, that are relevant to the performance index of (12) can be expressed by (suppressing the $s$ notation)

$$ G^o = \left(1 - A_u^o(I - C_{x2}^c A_u^{x2})^{-1} C_{x1}^c + C_{x2}^c A_u^{x2} X_1 \right) P^{x1} $$

$$ - A_{x1}^o P^{x1} \right)^{-1} A_u^o \left(1 - C_{x2}^c A_u^{x2} \right)^{-1} C_r^c, $$

(16)

$$ G^o = P^o G^o. $$

(17)

For the outer-loop controller in the modular control system, apply the causal and stabilizing control law

$$ V_{des}(s) = F^{-1}(s)A_u^o(s) \hat{U}(s) + A_{x1}^o(s)X_1(s), $$

(18)

where

$$ \hat{U}(s) = \left(I - C_{x2}^c A_u^{x2} \right)^{-1} \left(C_c^c R + \left(C_{x1}^c + C_{x2}^c A_u^{x2} \right) X_1 \right). $$

(19)
Note that $\dot{u}$ in (19) is identical to $u^*$ in (15) when $x_2$ is solved for in terms of $u$ and $x_1$. When ideal conditions are met (i.e., $V(s) = F(s)V_{des}(s)$), this yields

$$G^v = \left(1 - A_u^v (I - C_{x_2}^c A_{x_2}^u)^{-1} \left(C_{x_1}^c + C_{x_2}^c A_{x_1}^u\right) P_{x_1}^u\right) - A_{x_1}^v P_{x_1}^u\left(I - C_{x_2}^c A_{x_1}^u\right)^{-1} C_r^c,$$

which is identical to (16) and (17), thereby yielding $\gamma_0^p = \gamma_0^c$. Thus, it follows that $\gamma_0^p \leq \gamma_0^c \leq \gamma_m^*$. □

This simple inequality yields more profound implications when combined with the earlier reference-model-matching conditions of Proposition 4, leading to the following proposition.

**Proposition 7** (modular versus centralized equivalence conditions)
Suppose the states $x_2^m = x_2, F(s)$ is stable and a minimum phase, $\rho(F(s)) = \rho(A_u^v(s)), \rho(A_1^v(s)) < \rho(A_1^u(s))$, and $A_u^v(s)$ has no unstable zeros common to all $p$ transfer functions. Then, $\gamma_0^* = \gamma_0^c = \gamma_m^*$.

**Proof**
Suppose $F(s), C_{r}^o(s)$, and $C_{x_1}^o(s)$ are chosen optimally to yield $\gamma_0 = \gamma_0^c$. From Proposition 4, there exists a causal, stabilizing inner-loop controller (with transfer functions $C_{r}^v(s)$ and $C_{x_1}^v(s)$) that yields $V(s) = F(s)V_{des}(s)$, thus yielding $\gamma_m = \gamma_0 = \gamma_0^c$. It then follows immediately from Proposition 6 that $\gamma_m^* = \gamma_0^c = \gamma_0^*$. □

### 2.5. Summary—ideal modular control conditions

We now have conditions on the actuator dynamics and reference model that, when satisfied, simultaneously yield reference model matching and modular versus centralized equivalence (based on the performance index, $\gamma^P$, defined in Definition 3). These conditions are summarized as follows:

1. $F(s)$ is stable and a minimum phase.
2. $\rho(A_u^v(s)) < \rho(A_1^u(s))$.
3. $\rho(F(s)) = \rho(A_1^v(s))$.
4. $A_u^v(s)$ has no unstable zeros common to all $p$ constituent transfer functions.

In addition to providing a set of conditions on the actuator dynamics for which modular versus centralized equivalence can be achieved, these conditions also provide clear guidelines as to how the reference model, $F(s)$, should be chosen. Conditions (1) and (3) can always be satisfied with an appropriately designed reference model, whereas conditions (2) and (4) are dependent on the actuator dynamics themselves. Recognizing that modular control remains an appealing strategy even when these ideal conditions are not exactly met and when uncertainties render it impossible to match $F(s)$ exactly, in the remainder of this paper, we focus on mechanisms that reduce the gap between ideal and actual modular control performance. Because the impact of uncertainty on the overall system depends on one’s choice of $F(s)$, we will also focus on how $F(s)$ can be fine-tuned for a particular uncertainty model.

### 3. PERFORMANCE RECOVERY FORMULATION

Attributes of the actuator dynamics, in conjunction with inevitable modeling uncertainties, will typically result in a mismatch between ideal and actual closed-inner-loop performance. To perform a
more general analysis of system performance in the presence of this mismatch, we first express the closed inner loop through an uncertainty model:

\begin{align*}
T_1 &= F + W_1 \Delta_1, \\
T_2 &= W_2 \Delta_2,
\end{align*}

\( \| \Delta_1 \|_\infty \leq \Delta_1^{\text{max}} \),
\( \| \Delta_2 \|_\infty \leq \Delta_2^{\text{max}} \).

\( W_{1,2} \) are known stable transfer function matrices of size \( 1 \times 1 \) and \( 1 \times n_1 \), respectively, that capture information known about the mismatch between the actual and ideal inner-loop performance.

The design of weighting functions \( W_{1,2} \) in (22) represents an important design decision that will affect the feasibility of the forthcoming \( \mu \) synthesis design and the engineering value that such a design will have. We summarize general guidelines for the design of \( W_{1,2} \) and then show in Section 5 how these guidelines are applied on an application example.

To ensure well-posedness of the D-K iteration that is used in subsequent \( \mu \) synthesis design, we impose the following requirement on \( W_{1,2} \):

There does not exist a frequency,\( \omega \geq 0 \), at which \( W_2(j\omega) = 0 \) and \( W_{2i}(j\omega) = 0, i = 1 \ldots n \).

This requirement is not difficult to achieve and simply requires at least one weighting function to have a nonzero gain at any given frequency. Two engineering approaches can be used to choose a weighting function that is appropriate for the system at hand:

1. Simulation of likely parametric uncertainty: Given some physical understanding of the system at hand and the simplifications made in the modeling approach, one can identify likely sources of parametric uncertainty. The designer can then generate Bode plots of \( T_1 - F \) and \( T_2 \) under random variations in these uncertain parameters and shape \( W_1 \) and \( W_2 \) to match the shapes of these Bode plots, subject to well-posedness restrictions on \( W_{1,2} \).
2. Experimental uncertainty characterization: Given sufficiently rich experimental data for the closed-inner-loop system and the frequency content of \( v_{\text{des}} \) and \( x_1 \), one can also approximate the frequency domain behavior of \( T_1 - F \) and \( T_2 \) and shape \( W_{1,2} \).

To quantify the difference between the output of the nominal and actual (with uncertainty) closed-loop systems, we introduce a new variable, \( e' \), which is defined as follows:

\[ e' \triangleq y - y^r. \]

Here, \( y \) represents the actual output whereas \( y^r \) represents the output when (10) and (11) are satisfied. For notational convenience and in preparation for later \( \mu \) analysis and synthesis, we will cast both uncertainty sources (\( \Delta_1 \) and \( \Delta_2 \)) together in a block diagonal matrix, as follows:

\[ \Delta = \begin{bmatrix} \Delta_1 & 0 \\ 0 & \Delta_2 \end{bmatrix}. \]

Given this uncertainty representation, the closed-inner-loop error dynamics can be described by

\[ \bar{V} = \begin{bmatrix} W_1 & W_2 \end{bmatrix} \begin{bmatrix} \Delta_1 & 0 \\ 0 & \Delta_2 \end{bmatrix} \begin{bmatrix} V_{\text{des}} \\ X_1 \end{bmatrix}. \]

Remark 8
Although the uncertainty in (24) assumes a block diagonal structure, this structure is not restrictive in the sense that this structure allows every input to the inner closed loop (\( v_{\text{des}} \) and every element of \( x_1 \)) to affect the single output, \( v \), through an uncertain transfer function.

For performance recovery, we will make extensive use of the worst-case gain, \( \gamma^{\text{wc}} \), from \( r \) to \( e' \), where \( \gamma^{\text{wc}} \) is defined as

\[ \gamma^{\text{wc}} \triangleq \max_{\| \Delta \|_\infty \leq \Delta^{\text{max}} \| G^e \|_\infty}. \]
where

$$G^{er} \triangleq \frac{E'(s)}{R(s)}.$$

(27)

Estimation and analysis of worst-case performance in an uncertain system has been the topic of extensive literature (studied, for example, in [22]), and tools such as MATLAB’s Robust Control Toolbox have facilitated the estimation of $\gamma^{wc}$ for numerous robust control applications such as ours.

We propose a novel mechanism, referred to as MCEC, which is depicted in Figure 4 and is characterized by an add-on compensator, $C_v$. The proposed MCEC design structure exploits the signal $\tilde{v}$, which represents the difference between the virtual control input, $v$, and its nominal value, $v_{\text{des}}$ (the output of $F$), to reduce the gap between nominal and actual performance. The proposed strategy differs from other robust controller synthesis applications, such as in [17–20], which design the main controller to guarantee a certain level of overall system performance in the presence of uncertainty. Because the strategies take into consideration the nominal model as well as uncertainty in their design, they impose an inherent trade-off between nominal performance and performance in the presence of uncertainty. Our structure, employing $C_v$ as an add-on compensator, has two key advantages over strategies that redesign the main controller for robust performance:

(1) Our strategy retains the modularity of the inner and outer loops in the sense that it allows the two controllers to be designed in parallel without knowledge of each other;

(2) There is no trade-off between nominal performance and performance in the presence of uncertainty—when the inner-loop reference model is matched ($T_1(s) = F(s)$, $T_2(s) = 0$), $\tilde{v}$ will go to 0.

We show that by recasting the optimization problem for the MCEC design, we can formulate a $\mu$ synthesis problem to optimize specific worst-case performance on the basis of two synthesis objectives, S1 and S2:

S1 Given that $\|\Delta\|_{\infty} < 1$, find a stabilizing $C_v$ that minimizes $\gamma^{wc}$.

S2 Given $\gamma^{th} > 0$ ($\gamma^{th}$ is a performance threshold representing tolerable performance degradation), find $C_v$ that maximizes $\|\Delta\|_{\infty}$ such that the closed-loop system remains stable and $\gamma^{wc} < \gamma^{th}$.

4. MODULAR CONTROL ERROR COMPENSATION DESIGN FOR PERFORMANCE RECOVERY USING $\mu$ SYNTHESIS

To design $C_v$, the block diagram in Figure 4 is cast in the form given by Figure 5, where $\hat{P}$ is a transfer function matrix containing all of the system components besides $C_v$, $\Delta$, and scalar gain $k$.

Figure 4. Block diagram of the reference-model-based modular control design with modular control error compensation.
Figure 5. \( \mu \) synthesis performance formulation. Physical entities of the system are contained inside the dashed lines, whereas fictitious entities \((k, \Delta_P)\), which are strictly introduced for \( \mu \) synthesis design purposes, lie outside.

The expression for \( \tilde{P} \) is given in (29). Under this configuration, \( \mu \) synthesis considers an uncertainty structure given by

\[
\Delta_{\text{aug}} = \begin{bmatrix}
\Delta & 0 \\
0 & \Delta_P
\end{bmatrix}.
\] (28)

The following properties hold for the system in Figure 5 [23].

**Proposition 9 (robust performance interpretation of \( \mu \))**

Let \( \mu \) be the structured singular value for the system shown in the figure of Balas [23]. The following two properties hold:

1. The closed-loop system in Figure 5 is stable and \( k \| G^{\epsilon} \|_\infty < \mu \) for all uncertainties satisfying \( \| \Delta \|_\infty < 1/\mu \).
2. There exists a perturbation \( \Delta : \| \Delta \|_\infty = 1/\mu \) for which the closed-loop system in Figure 5 is unstable or \( k \| G^{\epsilon} \|_\infty = \mu \).

\[
\tilde{P} = \begin{bmatrix}
\frac{C^r_1 P^{s1} W_1}{1-C^r_1 P^{s1} F} & \frac{C^r_2 P^{s1} W_2}{1-C^r_2 P^{s1} F} & \frac{C^r}{1-C^r_1 P^{s1} F} & \frac{1}{1-C^r_1 P^{s1} F} \\ \\
\frac{I-P^{s1} FC^r_1}{P^{s1} W_1} & \frac{I-P^{s1} FC^r_2}{P^{s1} W_2} & \frac{I-P^{s1} FC^r}{P^{s1} F} & 0 \\
\frac{P^{s1} W_1}{W_1} & \frac{P^{s1} W_2}{W_2} & 0 & 0
\end{bmatrix}.
\] (29)

\( \mu \) synthesis optimizes \( C_v \) to minimize \( \mu \), which allows us to carry out objectives S1 and S2. This optimization is carried out through the so-called D-K iteration [23], which relies on an underlying iterative \( H_\infty \) optimization and is subject to the following well-posedness conditions:

1. The last row of \( \tilde{P}(j\omega) \) is right invertible for all \( \omega \geq 0 \).
2. The last column of \( \tilde{P}(j\omega) \) is left invertible for all \( \omega \geq 0 \).

Condition (1) can be satisfied through appropriate design of \( W_{1,2} \), as detailed in Section 4. Condition (2) requires the gain of at least one of the transfer functions in the last column of \( \tilde{P} \) to be nonzero at any given frequency.

Given that the well-posedness conditions have been satisfied, the following propositions show how one can develop a design algorithm to achieve the synthesis objectives by iteratively adjusting \( k \) and carrying out \( \mu \) synthesis to design \( C_v \).
Proposition 10 (synthesis objective S1)
Assume that S1 is feasible. Let $\mu^*(k, \hat{P})$ be the structured singular value of the system shown in Figure 5 where $C_v$ has been designed using $\mu$ synthesis, for a given constant $k$. Furthermore, let $k_1^*$ be the maximum value of $k$ such that $\mu^*(k, \hat{P}) = 1$. Then, $k_1^*$ exists. Furthermore, if $C_v$ is designed using $\mu$ synthesis, with $k = k_1^*$, then $\gamma_{wc}$ is minimized and the closed loop is stable, subject to $\|\Delta\|_\infty < 1$.

Proof
Existence: Given that S1 is feasible, there exists a stabilizing $C_v$ when $\|\Delta\|_\infty < 1$, which results in a finite $\gamma_{wc}$. When $k = 1/\gamma_{wc}$ is taken into account, it follows from Proposition 9 that $\mu(C_v, k, \hat{P}) = 1$, thereby proving the existence.

When $C_v$ is designed with $k = k_1^*$, the closed loop is stable and $\gamma_{wc}$ is minimized, subject to $\|\Delta\|_\infty < 1$: Closed-loop stability when $\mu^* = 1$ follows directly from property (1) of Proposition 9. To show that $\gamma_{wc}$ is minimized, we first show by contradiction that $\gamma_{wc} = \mu^*/k_1^*$. Suppose that $\gamma_{wc} < \mu^*/k_1^*$ ($\gamma_{wc} > \mu^*/k_1^*$ violates Proposition 9, property (1), and need not be considered).

Then, there exists $k > k_1^*$ that also yields $\mu^*(k, \hat{P}) = 1$, which contradicts the fact that $k_1^*$ is the maximum value of $k$ yielding $\mu^*(k, \hat{P}) = 1$. Therefore, $\gamma_{wc} = \mu^*/k_1^* = 1/k_1^*$. Because $k$ is maximized and $\mu$ synthesis minimizes $\mu$, $\gamma_{wc}$ is minimized for all $C_v, k$ that yield $\mu(C_v, k, \hat{P}) = 1$.

Finally, note that for all $C_v$ that yield a stable closed loop when $\|\Delta\|_\infty < 1$, there exists a $k$ such that $\mu(C_v, k, \hat{P}) = 1$. Therefore, minimizing $\gamma_{wc}$ over all $C_v, k$ that yield $\mu(C_v, k, \hat{P}) = 1$ is equivalent to minimizing $\gamma_{wc}$ over all $C_v$ that yield a stable closed loop when $\|\Delta\|_\infty < 1$. $\square$

Proposition 11 (synthesis objective S2)
Let $\mu^*(k, \hat{P})$ be the structured singular value of the system shown in Figure 5 where $C_v$ has been designed using $\mu$ synthesis, for a given constant $k$. Let $k_2^*$ be the minimum value of $k$ such that $\mu^*(k, \hat{P}) = k_2^{\gamma_{th}}$. Then $k_2^*$ exists, and if $C_v$ is designed with $k = k_2^*$, then $\mu$ synthesis maximizes the value of $\|\Delta\|_\infty$ such that $\gamma_{wc} < \gamma_{th}$ and the closed-loop system remains stable.

The proof is omitted but follows exactly the same logical sequence as the proof of Proposition 10. Using Propositions 9 and 10, we propose the following iterative algorithm to design the MCEC controller $C_v$ to achieve objectives S1 and S2; for S1, take $\mu^0 = 1$, and for S2, take $\mu^0 = k\gamma_{th}$:

(1) (a) Initialize $k_{\text{low}}$ to any value that is known to satisfy $k_{\text{low}} < k_i^*$ ($i = 1$ or $i = 2$ depending on the objective). Take $k_{\text{low}} = 0$ if no other lower bound is known. Proceed to step (1b).

(b) Initialize $k_{\text{high}}$ to any value that is known to satisfy $k_{\text{high}} > k_i^*$, and proceed to step (2). If no upper bound on $k$ is known, make an initial guess, $k_{\text{init}}$, and carry out $\mu$ synthesis for $k = k_{\text{init}}$.

(i) If $\mu^*(k_{\text{init}}, \hat{P}) - \mu^0 > \epsilon$, take $k_{\text{high}} = k_{\text{init}}$ and continue with step (2).

(ii) If $\mu^*(k_{\text{init}}, \hat{P}) - \mu^0 < \epsilon$, increase $k_{\text{init}}$ and repeat step (1b).

(2) Carry out $\mu$ synthesis for $k = 1/2(k_{\text{high}} + k_{\text{low}})$, which will return $C_v$ and $\mu^*$. Proceed to step (3).

(3) (a) If $|\mu^* - \mu^0| < \epsilon$, move to step (4) for S1 and step (5) for S2.

(b) If $\mu^* - \mu^0 < -\epsilon$, set $k_{\text{low}} = k$ and repeat step (2).

(c) If $\mu^* - \mu^0 > \epsilon$, set $k_{\text{high}} = k$ and repeat step (2).

(4) (a) If $|k_{\text{high}} - k| < \epsilon_k$, where $\epsilon_k$ is a user-defined threshold, terminate the algorithm.

(b) If $|k_{\text{high}} - k| \geq \epsilon_k$, take $k_{\text{low}} = k$ and move to step (4c).

(c) Take $k = 1/2(k_{\text{low}} + k_{\text{high}})$, and carry out $\mu$ synthesis.

(i) If $|\mu^*(k, \hat{P}) - \mu^0| \geq \epsilon$, set $k_{\text{high}} = k$ and repeat step (4c) until $|k_{\text{high}} - k| < \epsilon_k$.

(ii) Otherwise, take $k_{\text{low}} = k$ and repeat step (4c) until $|k_{\text{high}} - k| < \epsilon_k$ and then terminate.

(5) (a) If $|k - k_{\text{low}}| < \epsilon_k$, terminate the algorithm.

(b) If $|k - k_{\text{low}}| \geq \epsilon_k$, take $k_{\text{high}} = k$ and move to step (5c).

(c) Take $k = 1/2(k_{\text{low}} + k_{\text{high}})$, and carry out $\mu$ synthesis.

(i) If $|\mu^*(k, \hat{P}) - \mu^0| \geq \epsilon$, set $k_{\text{low}} = k$ and repeat step (5c) until $|k - k_{\text{low}}| < \epsilon_k$.

(ii) Otherwise, take $k_{\text{high}} = k$ and repeat step (5c) until $|k - k_{\text{low}}| < \epsilon_k$ and then terminate.
Remark 12
As an alternative to the iterative procedure proposed here, a skew \( \mu \) synthesis procedure may be employed, in which the size of one uncertainty block is held fixed whereas the other(s) are allowed to vary [24, 25]. This framework requires additional mathematical tools to set up but may result in reduced computational effort once implemented.

5. ENGINE THERMAL MANAGEMENT APPLICATION

The proposed modular control design approach is now studied on an engine thermal management system, which is used to facilitate engine mapping and calibration within an automotive test cell. The system, depicted in Figure 6, with a photograph of the actual experimental system given in Figure 7 (showing the unit containing the heater, heat exchanger, and mixing valve, which is connected through flexible piping to the engine), consists of parallel heating and cooling loops, used for controlling fluid temperature at the engine outlet. Readers are referred to [26] and [27] for system details, including a complete model description.

5.1. Modular system description and properties

The states in the thermal management system consist of eight temperatures, namely

- \( T_{\text{out,eng}} \) = engine outlet temperature,
- \( T_{\text{eng1}} \) = engine block temperature in the vicinity of coolant,
- \( T_{\text{eng2}} \) = engine block temperature in the vicinity of oil,
- \( T_{\text{out,mv}} \) = mixing valve outlet temperature,
- \( T_{\text{out,ht}} \) = heater outlet temperature,
- \( T_{\text{ht}} \) = heater coil temperature,
- \( T_{\text{out,hex}} \) = heat exchanger outlet temperature,
- \( T_{\text{out,cw}} \) = cooling water temperature at its outlet from the heat exchanger.

In this work, we utilize an effective general control strategy proposed in [27], where the heater is kept at a constant input of 2.25 kW, which has been deemed optimal for the speed and load that we consider in our experiments (2000 rpm and 75 N/m). We perform a linearization about \( T_{\text{out,eng}} = 80^\circ \text{C} \) (and the corresponding steady state), and the valve, with control input \( \phi \), is manipulated to achieve temperature tracking. We work with translated coordinates, that is, \( \delta T_{\text{out,eng}} = T_{\text{out,eng}} - T_{\text{out,eng}}^{\text{ss}} \), etc., as well as a translated control input, \( \delta \phi \), with the ultimate objective of controlling engine outlet temperature \( (T_{\text{out,eng}}) \) to a setpoint. The virtual control input is taken as \( T_{\text{out,mv}} \),
yielding the following modular system description:

\[
\begin{bmatrix}
\delta T_{\text{out,eng}} \\
\delta T_{\text{eng1}} \\
\delta T_{\text{eng2}} \\
\end{bmatrix}, \quad
\begin{bmatrix}
\delta T_{\text{out,mv}} \\
\delta T_{\text{out,ht}} \\
\delta T_{\text{out,hex}} \\
\delta T_{\text{out,cw}} \\
\end{bmatrix}, \quad
y = \delta T_{\text{out,eng}}, \quad v = \delta T_{\text{out,mv}}, \quad u = \delta \phi.
\]

For the linearized actuator dynamics, \(A_v(s)\) is minimum phase, \(\rho(A_v(s)) = 1\), and \(\rho(A_v(s)) = 2\). Thus, our modeled actuator dynamics satisfy the conditions for equivalence between optimal modular and centralized system performance, making this system an excellent candidate for reference-model-based modular control. Nevertheless, modeling uncertainties are present, and this will be the focal point of the MCEC design for the thermal management application.

5.2. Inner-loop reference model

For our system, we choose the simplest matchable reference model, namely

\[
F(s) = \frac{1}{\tau_f s + 1}.
\]

In our system analysis and experimental results, we will explore the effect of adjusting \(\tau_f\), both with and without MCEC in place.

5.3. Outer-loop control design

The objective of the outer-loop controller is to yield desirable overall system performance when the inner closed loop matches the reference model, that is, \(V(s) = F(s)\dot{V}_{\text{des}}(s)\). In our case, a simple outer-loop PI controller, given by

\[
\delta T_{\text{des}}^{\text{out,mv}} = \frac{2s + 0.05}{s} (\delta T_{\text{des}}^{\text{out,eng}}(s) - \delta T_{\text{out,eng}}(s)),
\]

yields excellent tracking performance when the reference model is matched.

5.4. Inner-loop design

Given our actuator model, it is possible to design the control law for \(\delta \phi\) such that \(\delta T_{\text{out,mv}}(s) = F(s)\delta T_{\text{des}}^{\text{out,mv}}(s)\) (hence achieving perfect model matching) by taking

\[
\delta \phi = k_1 \delta T_{\text{out,mv}}^{\text{des}} - k_2 \delta T_{\text{out,mv}} - k_3 \delta T_{\text{out,ht}} - k_4 \delta T_{\text{out,hex}},
\]
where \( \tau_f \) is the modeled time constant associated with the temperature dynamics of the mixing valve and \( k_1, k_2, k_3, \) and \( k_4 \) are directly determined from model parameters described in the detailed model in [27]. The values of \( k_1, k_2, k_3, \) and \( k_4 \) required for reference model matching are functions of the reference model time constant, \( \tau_f \).

### 5.5. Performance recovery in the presence of uncertainty—\( \mu \) synthesis setup

Given that our actuator model is not perfect, we now pursue an MCEC design that aims to keep the system within 10% of the ideal performance. Thus, we pursue synthesis objective S2 and take \( \gamma^0 = 0.1 \) to capture the 10% requirement.

To construct weighting functions, we consider three likely sources of uncertainty in the actuator model:

- Flow dynamics, where the flow distribution through the heater and the heat exchanger does not respond immediately, but rather through a time constant, \( \tau_{\text{flow}} \);
- Uncertain time constant, \( \tau_f \), associated with mixing valve thermal mixing dynamics;
- Inaccurate steady-state estimates, \( \phi^{ss}, T^{ss}_{\text{out,ht}}, \) and \( T^{ss}_{\text{out,hex}} \), which impact our linearized dynamics. We will see through experimental results that an uncertain value of \( \phi^{ss} \) is especially realistic for two reasons. First, the valve is not position controlled (it is pressure controlled and sometimes yields inconsistent response). Second, the ambient temperature affects the required value of \( \phi^{ss} \) to achieve a particular steady state (through the effect of ambient temperature on the cooling water temperature supplied to the system).

When augmented to include flow dynamics (33), the inner-loop dynamics that are affected by the uncertainty are given by

\[
\delta \phi = \frac{1}{\tau_{\text{flow}}} (\delta \phi + \delta u_{\text{mv}}), \tag{33}
\]

\[
\delta T_{\text{out,mv}} = \frac{1}{\tau_f} (\delta T_{\text{out,mv}} + \phi^{ss} \delta T_{\text{out,ht}} + (1 - \phi^{ss}) \delta T_{\text{out,hex}} + (T^{ss}_{\text{out,ht}} - T^{ss}_{\text{out,hex}}) \delta \phi), \tag{34}
\]

where the mixing valve command, \( u_{\text{mv}} \), does not immediately affect the valve position (and flow distribution), represented by \( \phi \). The uncertain values, their nominal values, and their uncertain ranges are given in Table I.

We consider these aforementioned uncertainty sources to design the weighting transfer functions, \( W_1(s) \) and \( W_2(s) \), that are necessary for our \( \mu \) synthesis design of \( C_v \). Analyzing the closed-loop effects of these uncertainties through Bode plots of closed-loop systems generated by randomly varying uncertain parameters over their pre-specified range, we have found that the

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Nominal value</th>
<th>Range</th>
</tr>
</thead>
<tbody>
<tr>
<td>( \tau_{\text{flow}} ) (s)</td>
<td>0.00</td>
<td>0.0 – 2.0</td>
</tr>
<tr>
<td>( \tau_f ) (s)</td>
<td>8.00</td>
<td>6.0 – 10.0</td>
</tr>
<tr>
<td>( \phi^{ss} )</td>
<td>0.75</td>
<td>0.7 – 0.8</td>
</tr>
<tr>
<td>( T^{ss}<em>{\text{out,ht}} - T^{ss}</em>{\text{out,hex}} ) (°C)</td>
<td>40.80</td>
<td>35.0 – 45.0</td>
</tr>
</tbody>
</table>

Table I. Uncertain actuator model parameters.
following weighting functions capture the frequency-dependent impact of the expected sources of system uncertainty:

\[
W_1(s) = \frac{0.2(0.1s + 1)(100s + 1)}{(0.3s + 1)(20s + 1)}, \quad (35)
\]

\[
W_2(s) = 0.1.
\]

Remark 13
Note that for the thermal management system, although there are three plant states, only one of these states has any effect on the actuator dynamics; thus, although \(W_2(s)\) is a \(3 \times 3\) transfer function matrix according to the formulation of (22), two of the entries in \(W_2(s)\) will be equal to 0. For simplicity, we drop these transfer functions from the formulation (the resulting \(\Delta\) is \(2 \times 2\)).

Remark 14
Nonzero DC content is essential to capture the true system uncertainty because there is no integrator in the inner loop (nor is one necessary because of the outer-loop integrator), and uncertainties will contribute to a DC error.

\[\mu\] synthesis was performed for several reference models (with different time constants, \(\tau_f\)), each leading to a different compensator, \(C_v(s)\). In spite of the differences and the high orders of the resulting compensators, each reflects a somewhat de-tuned and causal approximation of the compensator \(C_v(s) = 1/F(s)\), which results from setting \(G^{\text{ref}}(s) = 0\) and solving for \(C_v(s)\). In light of this fact, the resulting compensators were parameterized as

\[
C_v(s) = \frac{b_1 s + b_0}{a_2 s^2 + a_1 s + a_0}, \quad (36)
\]

where the transfer function coefficients were determined through a balanced truncation. The resulting worst-case performance levels, \(\gamma^{\text{wc}}\), under these (reduced-order) compensators, considering the aforementioned uncertainty sources, are given in Figure 8, for different values of \(\tau_f\).

Figure 8 shows that although MCEC provides benefit for all reference models, it is most helpful for aggressive reference models (i.e., fast time constants), where the inner-loop designer is pursuing a higher-bandwidth reference model, which exacerbates the effect of uncertainties on inner-closed-loop performance. Although the reference model is typically negotiated in the beginning stages of a control design project, the graph in Figure 8 can only be generated after the outer-loop controller

![Figure 8](image_url)

Figure 8. Performance comparison with and without modular control error compensation (MCEC), for different reference model time constants, based on speculated sources of uncertainty. In parameterizing the reference model, we take \(\tau_f = 0.25(2^n)\).
has been designed and closed-inner-loop uncertainty has been characterized (through weighting functions). Therefore, although it is evident from Figure 8 that \( F(s) = 1/4s + 1 \) represents a good reference model choice both with and without MCEC, this may not be known to a designer \textit{a priori}; thus, it is advantageous to have MCEC available because it results in a system performance that is more robust to one’s choice of reference model.

For very slow reference models, overall behavior degrades because the inner loop is designed to a bandwidth specification that is not well separated from that of the outer loop. This lack of bandwidth separation makes the overall system performance more sensitive to any mismatch between the reference model and actual inner-loop performance; thus, control performance both with and without MCEC deteriorates.

To gain deeper insight into the mechanisms by which MCEC improves performance, we can examine the Bode plots in Figures 9 and 10, which provide the open-loop transfer functions of the overall system (from where \( \delta T_{\text{eng}} \) enters the outer-loop controller to where \( \delta T_{\text{out}} \) is outputted from the plant), under worst-case uncertain parameter values and for \( \tau_f \) equal to 4 and 1 s, respectively. The figures indicate improved phase margins with the use of MCEC, particularly for the case of \( \tau_f = 1 \) s.

Whereas Figure 8 showed worst-case performance over a variety of reference models, Figures 11 and 12 show how worst-case performance varies with the level of uncertainty in system parameters. Specifically, Figures 11 and 12 provide worst-case performance levels when the parameters of Table I are allowed to vary over a specified percentage of their range (for example, for a value of 50%, \( \tau_{\text{flow}} \) may vary between 0 and 1 s, \( \tau_\phi \) may vary between 7 and 9 s, and \( \phi_s \) may vary between 0.725 and 0.775). To compare the use of MCEC with traditional applications of \( \mu \) synthesis, Figures 11 and 12 additionally show the worst-case gains when MCEC is not used but instead the outer-loop controller, \( C \), is designed using \( \mu \) synthesis. From these figures, it is evident that although standard \( \mu \) synthesis leads to improved performance in the presence of sufficiently large uncertainty, it also leads to a degradation in nominal performance, which is a trade-off that is not faced with MCEC.

5.6. Experimental results

The performance of the modular control strategy and MCEC was verified in an engine test cell, with the rapid prototyping setup using MATLAB Real-Time Workshop™ and xPC Target™. We consider temperature setpoint changes, where the engine is kept at constant speed and load (2000 rpm and 75 N/m, respectively). The plots given in Figures 13 and 14 show the data collected from

![Figure 9. Open-loop transfer function under worst-case uncertain parameters with \( \tau_f = 4s \). MCEC, modular control error compensation.](image-url)
Figure 10. Open-loop transfer function under worst-case uncertain parameters with $\tau_f = 1$ s. MCEC, modular control error compensation.

Figure 11. Worst-case performance under varied levels of uncertainty, for a reference model with $\tau_f = 4$ s. MCEC, modular control error compensation.

Figure 12. Worst-case performance under varied levels of uncertainty, for a reference model with $\tau_f = 1$ s. MCEC, modular control error compensation.
experiments, both with and without MEC. The results are summarized, along with model-based analysis results, in Table II, where experimental results are characterized quantitatively by the performance metric

\[
\gamma_{\text{exper}} = \frac{\left( \sum_{i=0}^{n} \left( \left( T_{\text{out,eng}}(i) - T_{\text{out,eng}}^{\text{des}}(i) \right)^2 \right) \right)^{1/2}}{\left( \sum_{i=0}^{n} \left( \left( T_{\text{out,eng}}^{\text{des}}(i) \right)^2 \right) \right)^{1/2}}.
\]  

(37)
Table II. Thermal management experimental and analysis results.

<table>
<thead>
<tr>
<th>$\tau_f$ (s)</th>
<th>$\gamma_{\text{exper}}$</th>
<th>$\gamma_{\text{exper}}^{\text{MCEC}}$</th>
<th>$\gamma_{\text{exper}}^{\text{No MCEC}}$</th>
<th>$\gamma_{\text{wc}}^{\text{MCEC}}$</th>
<th>$\gamma_{\text{wc}}^{\text{No MCEC}}$</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>0.074</td>
<td>0.129</td>
<td>0.098</td>
<td>0.274</td>
<td></td>
</tr>
<tr>
<td>4</td>
<td>0.072</td>
<td>0.087</td>
<td>0.072</td>
<td>0.127</td>
<td></td>
</tr>
</tbody>
</table>

MCEC, modular control error compensation.

where $\delta T_{\text{out,eng}}^r$ represents the engine outlet temperature (deviation from nominal) when the inner-loop reference model is matched exactly. This metric is intended to approximate the gain from $r$ to $e'$ for our experimental data.

As expected, $\gamma_{\text{exper}}$ is lower with MCEC than without under both reference models, but the difference is much more pronounced when $\tau_f = 1$ (the more aggressive reference model). Also, with MCEC, $\gamma_{\text{wc}}^{\text{MCEC}} < \gamma_{\text{th}}$ with both reference models, indicating that we have achieved acceptable performance with MCEC in both cases. For comparison, Table II also provides $\gamma_{\text{wc}}^{\text{No MCEC}}$ as predicted through an analysis of hypothesized sources of uncertainty. The experimental results correlate directionally well with the worst-case analysis results.

6. CONCLUSIONS AND FUTURE WORK

In this paper, we have proposed a reference-model-based modular control design framework. We analyzed the nominal performance of a modular control system, concluding that under certain ideal circumstances, it can perform as well as its centralized counterpart. To treat cases when the inner closed loop does not perform exactly as specified, we have developed a modular control error compensator (MCEC) for recovering desired performance. We have shown how $\mu$ synthesis may be used iteratively to achieve the design goals and have demonstrated the effectiveness of the proposed method on an engine thermal management system. Future work will include the development of deeper insights with regard to the trade-offs between the inner-loop performance specification, $F$, and the ability to achieve robust modular performance with the use of $C_v$. Additionally, the ability to incorporate inherently nonlinear characteristics (especially saturation) into closed-loop uncertainty models that are applicable to MCEC represents a great avenue (and challenge) for future investigation.

APPENDIX: PROOF OF PROPOSITION 4

Sufficiency: Take $\rho_1 = \rho \left( A_{\text{gu}}^v(s) \right)$ and $\rho_2 = \rho \left( A_{\text{gu}}^v(s) \right)$, and express $F(s)$ as

$$F(s) = \frac{N(s)}{D_1(s)D_2(s)}, \quad \text{(A.1)}$$

where $D_2(s)$ is $\rho_1^{\text{th}}$ order. Consider the control structure depicted in Figure A.15, which divides the controller into a feedback part that places $\rho_1$ poles and a feed-forward lead-lag element that places the zeros and remaining poles. The causality of the controller follows from condition (1) and Figure A.15, where $N(s)/D_1(s)$, $K$, and $L$ are all proper.

![Figure A.15. Block diagram of the inner-loop controller used in this model-matching proof.](image-url)
Perform an observable realization of the actuator dynamics; $V(s) = A_{w}^{	ext{T}}(s)U(s) + A_{x_1}^{	ext{T}}(s)X_1(s)$ results in
\[
\begin{bmatrix}
\dot{x}_{21} \\
\dot{x}_{22}
\end{bmatrix} = \begin{bmatrix}
A_{11} & A_{12} \\
A_{21} & A_{22}
\end{bmatrix}\begin{bmatrix}
x_{21} \\
x_{22}
\end{bmatrix} + \begin{bmatrix}
B_1 \\
B_2
\end{bmatrix}u + \begin{bmatrix}
E_1 \\
E_2
\end{bmatrix}x_1
\]
\[v = \begin{bmatrix} 1 & 0 & \ldots & 0 \end{bmatrix}\begin{bmatrix} x_{21} \\
\end{bmatrix}, \quad (A.2)
\]
where $\bar{x}_{21} \in \mathbb{R}^{n_1}$ and $\bar{x}_{22} \in \mathbb{R}^{n_2-n_1}$,
\[
B_1 = \begin{bmatrix}
0_{(p_1-1)x1} & \ldots & 0_{(p_1-1)x1}
\end{bmatrix},
\]
\[B_2 = \begin{bmatrix}
B_{21} \\
B_{22}
\end{bmatrix}, \quad (A.3)
\]
where $B_{2i} \in \mathbb{R}^{n-p_1}, \forall i$, and $b_1$ are scalars. From condition (2), $E_1 = 0$, which is essential for rejecting the $x_1$ disturbance. Define $u_a \triangleq u_1 + (b_2/b_1)u_2 + \ldots + (b_n/b_1)u_n, u_b = \begin{bmatrix} u_2 & \ldots & u_p \end{bmatrix}^T$ to obtain
\[
\begin{bmatrix}
\dot{x}_{21} \\
\dot{x}_{22}
\end{bmatrix} = \begin{bmatrix}
A_{11} & A_{12} \\
A_{21} & A_{22}
\end{bmatrix}\begin{bmatrix}
x_{21} \\
x_{22}
\end{bmatrix} + \begin{bmatrix}
B_{11} \\
B_{21}
\end{bmatrix}u_a + \begin{bmatrix}
0_{p_1 \times p-1} \\
B_{22}
\end{bmatrix}u_b + \begin{bmatrix}
0 \\
E_2
\end{bmatrix}x_1
\]
\[v = \begin{bmatrix} 1 & 0 & \ldots & 0 \end{bmatrix}\begin{bmatrix} x_{21} \\
\end{bmatrix}, \quad (A.5)
\]
where
\[
B_{11} = \begin{bmatrix} 0_{1 \times (p_1-1)} & b_1 \end{bmatrix}^T, \quad (A.6)
\]
\[
\bar{B}_{22} = \begin{bmatrix}
B_{22} - b_2/b_1 B_{21} & \ldots & B_{2p} - b_p/b_1 B_{21}
\end{bmatrix}. \quad (A.7)
\]
Now consider the control law
\[
u_a = L\bar{u}_{\text{des}} - K_{11}\bar{x}_{21} - K_{12}\bar{x}_{22}, \quad (A.8)
\]
\[
u_b = -K_{22}\bar{x}_{22}, \quad (A.9)
\]
where $K_{12} = \begin{bmatrix} -1/b_1 & 0 & \ldots & 0 \end{bmatrix}$, which yields the following closed-loop dynamics:
\[
\begin{bmatrix}
\dot{x}_{21} \\
\dot{x}_{22}
\end{bmatrix} = \begin{bmatrix}
A_{11} - \bar{B}_{11}K_{11} & 0_{p_1 \times (n_2-p_1)} \\
A_{21} - \bar{B}_{21}K_{11} & A_{22} - \bar{B}_{22}K_{22}
\end{bmatrix}\begin{bmatrix}
x_{21} \\
x_{22}
\end{bmatrix} + \begin{bmatrix}
B_{11} \\
B_{21}
\end{bmatrix}L\bar{u}_{\text{des}} + \begin{bmatrix}
0_{p_1 \times p-1} \\
\bar{B}_{22}
\end{bmatrix}u_b + \begin{bmatrix}
0 \\
E_2
\end{bmatrix}x_1
\]
\[v = \begin{bmatrix} 1 & 0 & \ldots & 0 \end{bmatrix}\begin{bmatrix} x_{21} \\
\end{bmatrix}, \quad (A.10)
\]
The $\bar{x}_{21}$ dynamics are decoupled from the $\bar{x}_{22}$ dynamics (and hence from $x_1$ and $u_b$). It is easily verified that $(A_{11}, B_{11})$ represents a controllable pair; hence, $K_{11}$ can be designed such that the eigenvalues of $A_{11} - \bar{B}_{11}K_{11}$ are equal to those of $D_2(s)$. Let $M(s)$ represent the transfer function vector from $u^*$ to $v$ for fixed $K_{11}$ and $K_{12}$, where $M_1(s)$ represents the first element of $M(s)$, and take
\[
L = \begin{bmatrix}
F(0)N(0) & 0 & \ldots & 0
\end{bmatrix}^T, \quad (A.11)
\]
to obtain $V(s) = F(s)V_{\text{des}}(s)$.
We are left to use $u_b$ to stabilize the zero dynamics, given by
\[
\dot{x}_{22} = (A_{22} - B_{21}K_{12})\tilde{x}_{22} + \tilde{B}_{22}u_b + (A_{21} - B_{21}K_{11})\tilde{x}_{21} + E_{22}x_1.
\]
(A.12)

To do this, first note that the eigenvalues of $A_{22} - B_{21}K_{12}$ are identical to the zeros of $A_{u1}^v(s)$. From condition (3), for every unstable zero, $\lambda_i$, of $A_{u1}^v(s)$, there exists $j$, such that $\lambda_j$ is not a zero of $A_{u1}^v(s)$ and is therefore also not a zero of $A_{u1}^v(s) - (b_j/b_1)A_{u1}^v(s)$. It follows that $(A_{22} - B_{21}K_{12}, \tilde{B}_{22})$ is a stabilizable pair, and therefore, there exists $K_{22}$ that stabilizes the zero dynamics of (A.12).

**Necessity:** To achieve $T_1(s) = F(s)$, $T_2 = 0$, we require
\[
A_u^v (I - C_{x2}^i A_{u2}^v)^{-1} C_{vdes}^i = F,
\]
(A.13)

\[
A_u^v (I - C_{x2}^i A_{u2}^v)^{-1} C_{vdes}^i A_{x1}^{x2} = -A_{x1}^v.
\]
(A.14)

The necessity of conditions (1) and (2) is obvious from comparing (A.13) and (A.14) with the following facts:
\[
\rho \left( A_u^v (I - C_{x2}^i A_{u2}^v)^{-1} C_{vdes}^i \right) \geq \rho \left( A_u^v \right) > \rho (F),
\]
\[
\rho \left( A_u^v (I - C_{x2}^i A_{u2}^v)^{-1} C_{vdes}^i A_{x1}^{x2} \right) > \rho \left( A_u^v \right) \geq \rho \left( A_u^v \right).
\]

Clearly, (A.13) and (A.14) cannot be achieved if the relative degrees of the left-hand and right-hand sides of (A.13) and (A.14) cannot even be made equal to each other.

Finally, suppose that there exists $\lambda$ such that $\text{Re}(\lambda) > 0$ and $A_{u,j}^v(\lambda) = 0$, $\forall j$. Then, $A_u^v$ can be factored as $A_u^v(s) = (s + \lambda)\hat{A}_u^v(s)$, and (A.13) can be written as
\[
(s + \lambda)\hat{A}_u^v (I - C_{x2}^i A_{u2}^v)^{-1} C_{vdes}^i = F,
\]
(A.15)

and for (A.15) to hold for any stable $F$, $\lambda$ must be canceled with an unstable pole (at $\lambda$), thereby destabilizing the actuator dynamics.

**ACKNOWLEDGEMENTS**

The authors wish to acknowledge Professors Le Yi Wang and Jim Freudenberg of the Electrical and Computer Engineering Department at Wayne State University and the Electrical Engineering and Computer Science Department at the University of Michigan, respectively, for discussions and insight regarding robust modular control results and $\mu$ synthesis. This work was supported by Toyota Motor Engineering and Manufacturing, North America (TEMNA).

**REFERENCES**


17. Boulet B, Francis BA, Hughes PC, Hong T. $\mu$ synthesis for a large flexible space structure experimental testbed. *Journal of Guidance, Control, and Dynamics* 2001; **24**.